

Notice of References Cited	Application/Control No. 10/765,481	Applicant(s)/Patent Under Reexamination SHIRLEY ET AL.	
	Examiner Fernando L. Toledo	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber; Silicon Processing for the VLSI Era Volume 1: Process Technology; copyright 1986, Lattice Press; Sunset Beach, CA; pages 429, 434-437, 452 and 453.
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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